WHAT IS CLAIMED IS:

1. A radio communication control device comprising:

a demodulation unit configured to demodulate

5   a received signal;

a detection circuit configured to detect final data contained in a received data stream supplied from the demodulation unit, said detection circuit outputting a final data notification signal when

10 detecting the final data; and

a standby period timer configured to set a standby period in accordance with the final data notification signal output from said detection circuit.

2. The device according to claim 1, wherein the

15 received data stream includes:

a data section containing a plurality of symbols; and

a symbol length indicating the number of symbols contained in the data section.

20 3. The device according to claim 2, wherein said detection circuit includes:

an arithmetic operation circuit configured to calculate the number of symbols from the symbol length contained in the received data stream;

25 a register configured to hold the number of symbols supplied from said arithmetic operation circuit;

a counter configured to count the number of symbols contained in the received data stream; and

a comparator configured to compare the number of symbols counted by the counter and the number of

5　symbols held by the register with each other, said comparator outputting the final data notification signal when these numbers coincide with each other.

4.　The device according to claim 1, wherein the standby period timer subtracts a start delay time of

10　the standby period timer and a delay time for a data transmission process from a standby period defined by a specification, in accordance with the final data notification signal, and obtains an actual standby period.

15　5.　The device according to claim 4, wherein the standby period timer further comprises:

a subtracter configured to subtract a start delay time of the standby period timer and a delay time for a data transmission process from a standby period

20　defined by a specification, in accordance with the final data notification signal, and to obtain an actual standby period;

an adder configured to add a present time to the actual standby period supplied from said subtracter;

25　and

a comparator configured to compare the time outputted from the adder and the present time with each

other, said comparator outputting a signal when both times coincide with each other.

6. The device according to claim 3, further comprising:

5   a buffer circuit connected to an output terminal of the detection circuit, and configured to hold symbols outputted from said detection circuit;

a Viterbi decoder connected to an output terminal of the buffer circuit, and configured to decode the

10   symbols outputted from said detection circuit, to reproduce a frame; and

a receiver unit configured to receive the frame outputted from said Viterbi decoder.

7. The device according to claim 1, further

15   comprising:

a transmitter unit connected to the standby period timer, and configured to transmit a frame in accordance with an output signal of the standby period timer.

8. A radio communication control device

20   comprising:

a demodulation unit configured to demodulate a received signal;

a detection circuit configured to count the number of symbols contained in a received data stream supplied

25   from said demodulation unit, said detection circuit outputting a final data notification signal when the counted number becomes equal to a predetermined symbol

number;

a standby period timer configured to set a standby period in accordance with the final data notification signal output from said detection circuit.

9. The device according to claim 8, wherein the received data stream includes:

a data section containing a plurality of symbols; and

a symbol length indicating the number of symbols contained in the data section.

10. The device according to claim 8, wherein said detection circuit includes:

an arithmetic operation circuit configured to calculate the number of symbols from the symbol length contained in the received data stream;

a register configured to hold the number of symbols supplied from the arithmetic operation circuit;

a counter configured to count the number of symbols contained in the received data stream; and

a comparator configured to compare the number of symbols counted by the counter and the number of symbols held by the register with each other, said comparator outputting the final data notification signal when these numbers coincide with each other.

11. The device according to claim 8, wherein the standby period timer subtracts a start delay time of the standby period timer and a delay time for a data

transmission process from a standby period defined by a specification, in accordance with the final data notification signal, and obtains an actual standby period.

5   12.   The device according to claim 11, wherein the standby period timer further comprises:

a subtracter configured to subtract a start delay time of the standby period timer and a delay time for a data transmission process from a standby period defined 10   by a specification, in accordance with the final data notification signal, and to obtain an actual standby period;

an adder configured to add a present time to the actual standby period supplied from the subtracter; and

15   a comparator configured to compare the time outputted from the adder and the present time with each other, the comparator outputting a signal when both times coincide with each other.

13.   The device according to claim 10, further 20   comprising:

a buffer circuit connected to an output terminal of the detection circuit, and configured to hold symbols outputted from the detection circuit;

a Viterbi decoder connected to an output terminal 25   of the buffer circuit, and configured to decode the symbols outputted from the detection circuit, to reproduce a frame; and

a receiver unit configured to receive the frame outputted from the Viterbi decoder.

14. The device according to claim 8, further comprising:

a transmitter unit connected to the standby period timer, and configured to transmit a frame in accordance with an output signal of the standby period timer.

15. A radio communication control device which starts data transmission when a predetermined time elapses counting from reception of transmission data, said device comprising:

a demodulation unit configured to demodulate a received signal;

a detection circuit configured to count the number of symbols contained in a received data stream supplied from said demodulation unit, said detection circuit outputting a final data notification signal when the counted number becomes equal to a predetermined symbol number;

a standby period timer configured to set a standby period in accordance with the final data notification signal output from said detection circuit.

16. The device according to claim 15, wherein the received data stream includes:

a data section containing a plurality of symbols; and

a symbol length indicating the number of symbols

contained in the data section.

17. The device according to claim 16, wherein said detection circuit includes:

an arithmetic operation circuit configured to calculate the number of symbols from the symbol length contained in the received data stream;

a register configured to hold the number of symbols supplied from the arithmetic operation circuit;

a counter configured to count the number of symbols contained in the received data stream; and

a comparator configured to compare the number of symbols counted by the counter and the number of symbols held by the register with each other, said comparator outputting the final data notification signal when these numbers coincide with each other.

18. The device according to claim 15, wherein the standby period timer subtracts a start delay time of the standby period timer and a delay time for a data transmission process from a standby period defined by a specification, in accordance with the final data notification signal, and obtains an actual standby period.

19. The device according to claim 18, wherein the standby period timer further comprises:

a subtracter configured to subtract a start delay time of the standby period timer and a delay time for a data transmission process from a standby period defined

by a specification, in accordance with the final data
notification signal, and to obtain an actual standby
period;

5 an adder configured to add a present time to the
actual standby period supplied from the subtracter; and

a comparator configured to compare the time
outputted from the adder and the present time with each
other, the comparator outputting a signal when both
times coincide with each other.

10 20. The device according to claim 15, further
comprising:

a buffer circuit connected to an output terminal
of the detection circuit, and configured to hold
symbols outputted from the detection circuit;

15 a Viterbi decoder connected to an output terminal
of the buffer circuit, and configured to decode the
symbols outputted from the detection circuit, to
reproduce a frame; and

a frame receiver unit configured to receive the
20 frame outputted from the Viterbi decoder.